



DEFENSE LOGISTICS AGENCY
DEFENSE SUPPLY CENTER, COLUMBUS
POST OFFICE BOX 3990
COLUMBUS, OH 43218-3990

IN REPLY
REFER TO

DSCC-VQ (VQC-05-007333/Mr. Tran/614-692-0606/mjg)

NOV 10 2004

SUBJECT: Level V and Q Non Radiation Hardness Certification, MIL-PRF-38535, FSC 5962

Mr. John Stannard
President
QP Semiconductor
2945 Oakmead Village Court
Santa Clara, CA 95051

Dear Mr. Stannard:

QP Semiconductor has demonstrated to the Defense Supply Center, Columbus (DSCC) that it complies with MIL-PRF-38535, the performance specification used by the Department of Defense for monolithic integrated circuits.

QP Semiconductor is granted full V and Q level certification for the technology flows used for the quality assurance levels listed in the enclosure. This certification and listing includes the subcontractors approved by QP Semiconductor. This letter supersedes DSCC letter (VQC-03-004391) to reflect the current certification status of QP Semiconductor as documented on the Quality Management Plan (QMP) QAM-1, Rev D., dated Jun 23, 2004.

In addition, the parts that are manufactured using the certified technology flows are being listed on the QML-38535. This will allow QP Semiconductor to mark parts with "Q" or "QML". These designators have been authorized by the Department of Defense for parts that have been produced to a QML specification, (i.e., one which allows less government oversight), the use of world-wide commercial production lines, reduced finished product testing based on statistical process controls (SPC), and other cost advantages.

Testing must be performed using the facilities and methods listed in the Laboratory Suitability letter DSCC-VQC-05-007332, or at facilities approved by the QP Semiconductor Technical Review Board (TRB) using its MIL-PRF-38535 Quality Management Program Plan.

This certification is subject to the conditions in DoD 4120.24-M, Defense Standardization Program.

Any and all of the facilities mentioned on the enclosure are subject to an audit by the Qualifying Activity at any time. The manufacturer shall be responsible for all audit expenses incurred for the offshore facilities. Offshore facilities are subject to all of the conditions of MIL-PRF-38535, Appendix E.

QPL/QML manufacturers shall notify the Qualifying Activity immediately after learning of a potential issuance of a GIDEP alert, problem advisory or major quality/reliability problem on their QPL/QML products. Failure to provide prior notification may be grounds for removal from QML-38535.

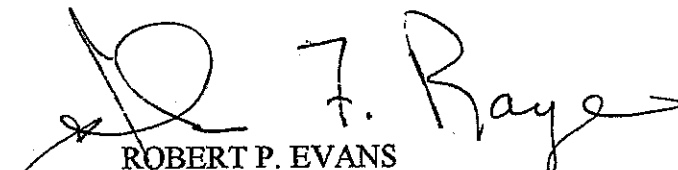
In addition, it is requested that the following activities be reported promptly to DSCC:

- Changes to certified facilities, process flows, or approved testing subcontractors
- Problem evaluation and a corrective action when:
 - a. A Technology Conformance Inspection (TCI) failure has been validated
 - b. The reliability of shipped parts is questionable.
- Test optimization, including:
 - a. Implementation - paragraph J.3.12, Appendix J, MIL-PRF-38535
 - b. Changing, suspending or canceling a prior test optimization
- Additions or deletions of parts in the QML-38535
- Change of company QML contact or other key QML personnel

This certification is valid until terminated by written notice from the Qualifying Activity. If warranted, it may be withdrawn by this Center at any time.

If you have any questions, please contact Mr. Tran at (614) 692-0606.

Sincerely,


ROBERT P. EVANS
Chief
Sourcing and Qualification Unit

Enclosure

cc:

QP Semiconductor (Mr. Bill Bennett)

VQC (Scott Thomas)

VQC (Michael Grammens)

<u>OPERATION</u>	<u>FACILITY</u>	<u>LOCATION</u>	<u>TECHNOLOGY AND PROCESS FLOW</u>
Design:	QP Semi	Santa Clara, CA	As defined in QP Semi Doc SCD-14, "Device Design Requirements."
	Desert Technology	Tempe, AZ	As defined in QP Semi Doc SCD-14, "Device Design Requirements."
Wafer Fabrication:	X-Fab	Lubbock, TX	As defined in QP Semi Doc SCD-18, "Wafer Fab Baseline, X-Fab."
	Medtronic	Tempe, AZ	As defined in QP Semi Doc SCD-16, "Wafer Fab Baseline, Medtronic Microelectronic Center."
	Polar Fab	Bloomington, MN	As defined in QP Semi Doc SCD-15, "Die/Wafer Requirements Foundry."
Assembly:	QP Semi	Santa Clara, CA	As defined in QP Semi Doc SCD-12, "Assembly Build Requirements."
	Millenium	Bangkok, Thailand	As baselined in MMT Doc QPL-AH-001, "QP-Labs Process Flow and Special Instructions."
	Certek Electronics Corporation	Laguna, Philippines	As defined in QP Semi Doc SCD-12, "Assembly Build Requirements"
Tests/Screening:	As baselined in the Laboratory Suitability letter	As baselined in the Laboratory Suitability letter	As baselined in the Laboratory Suitability letter DSCC-VQC-03-004392
QCI	QP Semi	Santa Clara, CA	All QML Products
Misc. Operations:	As baselined in the Laboratory Suitability letter DSCC-VQC-03-004392	As baselined in the Laboratory Suitability letter DSCC-VQC-03-004392	As baselined in the Laboratory Suitability letter DSCC-VQC-03-004392

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